

APPLICATION NO. 10/0580005

August 4, 2004

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CLMPTO

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1.-2. (Cancelled)

3. (Currently Amended) ~~[[The]]~~ A semiconductor integrated circuit as claimed in claim 1, comprising:

pads;

a first power supply I/O cell which is connected to a first pad of said pads, said first pad being provided corresponding to said first power supply I/O cell;

a wire line that is connected to said first power supply I/O cell;

a second power supply I/O cell which is connected to a second pad of said pads and is connected to said wire line to receive power supply from said first power supply I/O cell, said second pad being provided corresponding to said second power supply I/O cell and being different from said first pad;

an internal cell;

a power supply line which provides power supply to said internal cell, wherein said first power supply I/O cell and said second power supply I/O cell are each connected to said power supply line.

wherein said first pad is connected to at least one external pin and wherein said second pad is not connected to any of the at least one external pin, and

wherein said second power supply I/O cell is not connected to the corresponding one of pads that corresponds to said second power supply I/O cell.

CLAIMS 4-7 (CANCELLED)